

EXTERNAL I/O DISPLAY SECTION MEMORY 8 20 HYBRID SECTION 9 1x EVDO RADIO PROCESSING SECTION 2 SWITCHING SECTION 8 RADIO SECTION 2 BASE STATION B BASE STATION A

FIG. 1

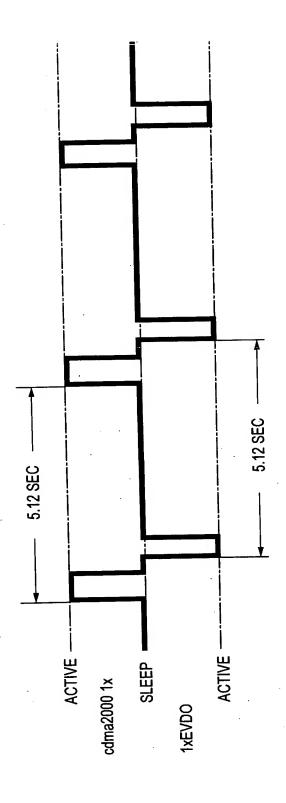


FIG. 2

FIG. 3A

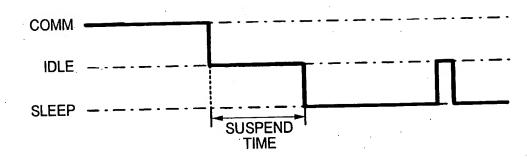


FIG. 3B

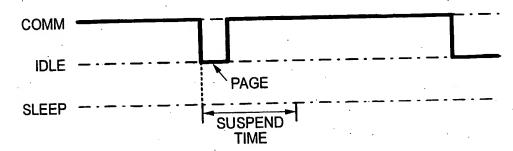
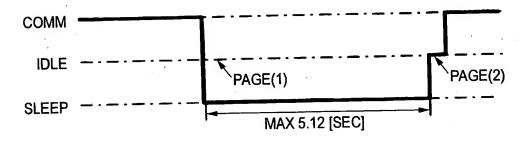


FIG. 3C



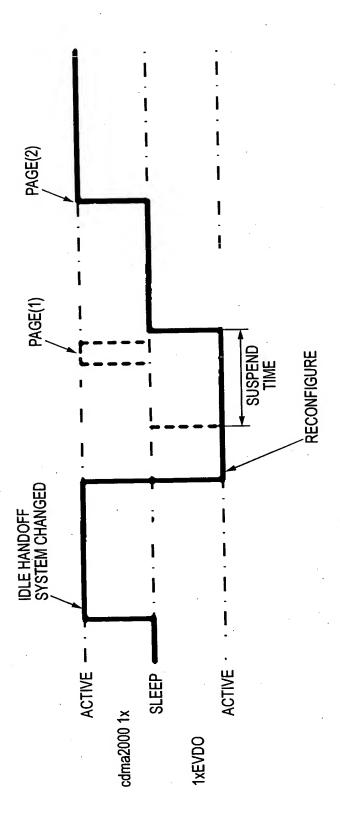
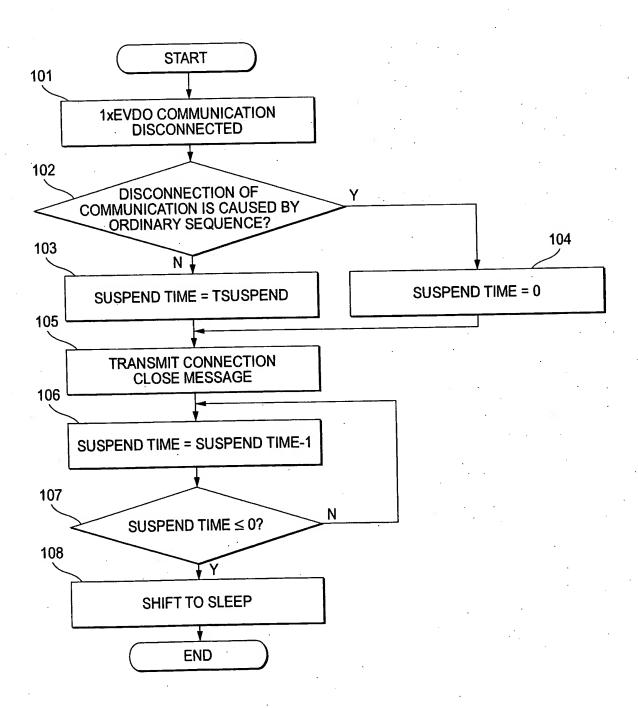
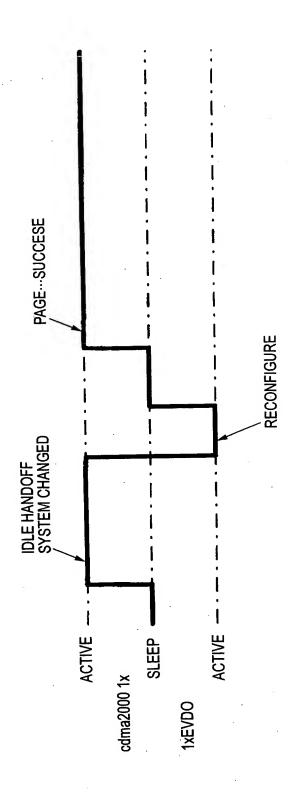


FIG. 4

FIG. 5





⁻1G. 6